

### **Amendments To The Claims**

The listing of claims presented below will replace all prior versions, and listings, of claims in the application.

#### **Listing of claims:**

1. (currently amended) A vertical alignment mode liquid crystal display device which comprises:

upper and lower substrates which are disposed opposite one another at the desired interval;

a liquid crystal layer sandwiched between the upper and lower substrates and formed of liquid crystals with negative dielectric anisotropy;

an insulating film formed on the inner surface of the lower substrate and having a hole formed therein;

a jagged pixel electrode which is formed on the insulating film, wherein in such a manner that the pixel electrode partially but not completely covers ~~more than half of~~ the hole;

a counter electrode which is formed on the inner surface of the upper ~~electrode~~ substrate;

vertical alignment films which are interposed between the pixel electrode and the liquid crystal layer and between the counter electrode and the liquid crystal layer, respectively; and

polarizers which are attached on the outer surfaces of the upper and lower substrates, respectively, in such a manner that their polarizing axes cross each other.

2. (original) The vertical alignment mode liquid crystal display device of Claim 1, wherein the hole is a rectangular hole.
3. (original) The vertical alignment mode liquid crystal display device of Claim 2, wherein the hole has a height of less than 2  $\mu\text{m}$ , a length of shorter side of less than 5  $\mu\text{m}$ , and a wall angle of 10-90 degrees.
4. (original) The vertical alignment mode liquid crystal display device of Claim 1, wherein the pixel electrode is formed in such a manner that the ratio between a portion of the hole covered with the pixel electrode and a portion of the hole non-covered with the pixel electrode is 1:1-5:1.
5. (original) The vertical alignment mode liquid crystal display device of Claim 1, wherein the pixel electrode is formed in such a manner that the interval between two adjacent pixel electrodes is less than 10  $\mu\text{m}$ .
6. (original) The vertical alignment mode liquid crystal display device of Claim 1, wherein the hole is positioned at both of a concave portion and a convex portion of the jagged pixel electrode.
7. (original) The vertical alignment mode liquid crystal display device of Claim 1, which further comprises phase compensation plates interposed between the upper substrate and the adjacent polarizer and between the lower substrate and the adjacent polarizer.
8. (original) The vertical alignment mode liquid crystal display device of Claim 7, wherein the phase compensation plates are monoaxial phase compensation plates or biaxial phase compensation plates, in which the monoaxial phase compensation plates have a phase delay value ranging from 40 to 800 nm, and the biaxial phase

compensation plates have a phase delay value ranging from 150 to 250 nm.

**9 7. (currently amended)** The vertical alignment mode liquid crystal display device of Claim 1, wherein the liquid crystals have a dielectric anisotropy of  $-2$  to  $-10$ .

**10 8. (currently amended)** The vertical alignment mode liquid crystal display device of Claim 1, wherein the liquid crystal has a thickness of  $2\text{-}6\text{ }\mu\text{m}$ , and the thickness of the liquid crystal layer times the refractive index anisotropy of the liquid crystals is 200-500 nm.

**11. (new)** The vertical alignment mode liquid crystal display device of Claim 1, wherein the liquid crystal layer come in contact with the lower substrate through the uncovered portion of the hole.